

# Mo-Doped La<sub>2</sub>O<sub>3</sub> as Charge-Trapping Layer for Improved Low-Voltage Flash-Memory Performance

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## Abstract

To utilize the negative conduction-band offset of high-*K* MoO<sub>3</sub> with respect to Si, the memory characteristics of high-*K* La<sub>2</sub>O<sub>3</sub> with and without Mo doping as charge-trapping layer were investigated. The cross-sectional structure of the memory was studied by transmission electron microscopy, and the chemical composition of the charge-trapping layer was investigated by X-ray photoelectron spectroscopy. Compared to the memory with pure La<sub>2</sub>O<sub>3</sub>, the one with Mo-doped La<sub>2</sub>O<sub>3</sub> had a significantly large C-V hysteresis window, much higher programming/erasing speeds and better charge retention because the Mo doping introduced many deeper-level traps and the LaMoO charge-trapping layer provided deeper quantum wells.

## Introduction

Metal-oxide-nitride-oxide-silicon (MONOS)-type memories have attracted significant interest due to their lower operating voltage, higher program/erase (P/E) speeds, higher reliability and stronger scaling ability.<sup>1-3</sup> Many high-*K* dielectrics, especially rare-earth metal oxide materials, e.g. Pr<sub>2</sub>O<sub>3</sub> (~15),<sup>4</sup> Nd<sub>2</sub>O<sub>3</sub> (~16),<sup>4</sup> Er<sub>2</sub>O<sub>3</sub> (~13),<sup>4</sup> and La<sub>2</sub>O<sub>3</sub> (~27),<sup>5</sup> have been investigated as charge-trapping layer (CTL) due to their moderately high dielectric constant, wide bandgap and good electrical properties.<sup>6</sup> Among these dielectric materials, La<sub>2</sub>O<sub>3</sub> is a promising one due to its large dielectric constant, thermodynamic compatibility with Si, and deep-level traps.<sup>7</sup> However, it has been reported that MONOS memory with La<sub>2</sub>O<sub>3</sub> as CTL possesses a relatively small window due to its low trap density.<sup>5</sup> Recently, MoO<sub>x</sub> has been proposed as the charge trapping site for nanocrystal memory and resulted in good memory performances, such as low operating voltage and good charge retention.<sup>8, 9</sup> However, for practical applications, uniform deposition or self-assembly of small nanocrystals is a necessary condition.<sup>10-14</sup> On the other hand, the application of MoO<sub>3</sub> for MONOS-type memory was seldom studied, even though MoO<sub>3</sub> has large dielectric constant (30~40),<sup>15</sup> many oxygen deficiencies as charge traps,<sup>16</sup> and large negative conduction-band offset (NCBO) with respect to Si.<sup>17</sup> Thus, in order to combine the advantages of both La<sub>2</sub>O<sub>3</sub> and MoO<sub>3</sub>, in this work, we propose Mo-doped La<sub>2</sub>O<sub>3</sub> as a new CTL and study its memory characteristics for potential flash memory applications.

## Experiment

MONOS capacitors were fabricated on p-type silicon wafers with a resistivity of 5 ~ 10 Ω·cm. Prior to the thermal growth of tunnel layer (SiO<sub>2</sub>) in dry oxygen at 900 °C,

the Si wafers were cleaned by the standard RCA method. Then, sputtering was employed for the growth of CTL. La<sub>2</sub>O<sub>3</sub> CTL (denoted as LaO sample) was deposited by sputtering of La metal target (RF = 30 W) in an Ar/O<sub>2</sub> (24:3) mixture at 2.1 mTorr for 4,200 s, while LaMoO CTL (denoted as LaMO sample) was deposited by co-sputtering of La metal target (RF = 30 W) and Mo metal target (DC = 0.01 A) in the same ambient for 2,700 s. After that, Al<sub>2</sub>O<sub>3</sub> film as blocking layer was grown by means of atomic layer deposition. Subsequently, both samples went through post-deposition annealing at 900 °C in N<sub>2</sub> for 30 s. Following that, Al was thermally evaporated and patterned as gate electrode. Finally, the samples after Al evaporation on the wafer backside went through a forming-gas annealing at 300 °C for 20 min. The chemical composition of the CTLs in the MONOS capacitors was analyzed by X-ray photoelectron spectroscopy (XPS), while the physical thicknesses of the layers in the MONOS capacitor structure were confirmed by transmission electron microscopy (TEM).

## Results and discussion

The structure of the MONOS capacitor is displayed in Fig. 1 (a). From the cross-sectional TEM image, it is observed that the physical thickness of the tunnel layer (SiO<sub>2</sub>), the charge-trapping layer (LaMoO) and the blocking layer (Al<sub>2</sub>O<sub>3</sub>) is 4.2 nm, 6.5 nm and 14.5 nm respectively. The TEM image also verifies that the LaMoO film maintains amorphous structure after the PDA at 900 °C, and thus is suitable to be a CTL. To fairly compare the electrical performance of the two samples, the physical thickness of La<sub>2</sub>O<sub>3</sub> (6.1 nm) is similar to that of LaMoO. The chemical binding states of both CTLs are investigated by XPS. Fig. 1 (b) shows the La 3*d* XPS spectrum of the La<sub>2</sub>O<sub>3</sub> film and the LaMoO film. For the La<sub>2</sub>O<sub>3</sub> film, there are two peaks of La 3*d*

located at 834.2 eV and 850.9 eV, each possessing a satellite peak due to charge transfer between the metal atom and its oxygen ligand.<sup>18</sup> For the LaMoO film, both peaks of La 3*d* shift by 0.5 eV to higher values at 834.7 eV and 851.4 eV, which are consistent with the La component in binary metal oxide.<sup>18</sup> The possible reason for this shift is the oxygen reduction in the bulk of the LaMoO film due to the Mo incorporation. However, more studies are required to further investigate this phenomenon. Fig. 1 (c) also depicts the Mo 3*d* core level spectra of the LaMoO film. After fitting, two doublets are observed, one located at a higher binding energy of 235.6 eV (Mo 3*d*<sub>3/2</sub>) and 232.4 eV (Mo 3*d*<sub>5/2</sub>), while the other located at a lower binding energy of 234.9 eV (Mo 3*d*<sub>3/2</sub>) and 231.7 eV (Mo 3*d*<sub>5/2</sub>). It has been demonstrated that these peaks should result from the chemical states of Mo<sup>6+</sup> and Mo<sup>5+</sup> respectively,<sup>16</sup> and electrons can be trapped at Mo<sup>5+</sup> sites in the vicinity of oxygen vacancies.<sup>15</sup> As explained above, these oxygen vacancies also reduce the oxygen content in the bulk of the LaMoO film, thus possibly resulting in a shift of the La 3*d* binding energy to higher values.

Fig. 2 shows the C-V hysteresis of the LaO sample and LaMO sample. The LaO sample has a smaller hysteresis window of 1.8 V under a +/-8 V sweep voltage. However, for the LaMO sample, the hysteresis window reaches 3.1 V even under a lower sweep voltage of +/-6 V, and further increases to 5.6 V and 7.1 V under +/-8 V and +/-10 V sweep respectively. The significantly larger C-V hysteresis window (about 3 times for the +/-8 V sweep) of the LaMO sample means that the Mo doping introduces many new traps in its CTL. Moreover, the *K* value of La<sub>2</sub>O<sub>3</sub> film and LaMoO film can be calculated to be 12.8 and 16.5 respectively, by assuming a dielectric constant of 9 for the Al<sub>2</sub>O<sub>3</sub> blocking layer. In addition, the Mo incorporation hardly affects the gate leakage current, which is  $4.0 \times 10^{-7}$  A/cm<sup>2</sup> and  $3.8 \times 10^{-7}$

A/cm<sup>2</sup> at 10 V for the LaO and LaMO samples respectively. At 14 V, the same gate leakage current is  $6.5 \times 10^{-7}$  A/cm<sup>2</sup> and  $1.2 \times 10^{-6}$  A/cm<sup>2</sup> respectively.

The transient program and erase characteristics of the two samples are depicted in Fig. 3 (a) and (b) respectively. As can be seen, the LaMO sample has much higher programming speed than the LaO sample at the same applied voltage for the same pulse time. For the LaO sample at a program voltage of +14 V for a pulse time of 10 ms, 100 ms and 1 s, the  $V_{th}$  shift is only 2.78 V, 3.54 V and 3.98 V respectively, while the corresponding shift of the LaMO sample is much larger: 5.87 V, 6.18 V and 6.50 V. Even at a lower program voltage of +10 V, the LaMO sample can still produce a larger shift of 3.88 V, 4.26 V and 4.50 V. This observation is consistent with the distinct C-V hysteresis difference of the two samples shown in Fig. 2. The faster programming characteristic of the LaMO sample should result from (i) the higher trap density in its CTL induced by the Mo doping because the Mo<sup>5+</sup> sites in the vicinity of oxygen vacancies can act as charge traps (as explained earlier); (ii) the deeper quantum wells built by the energy-band alignment of the LaMoO CTL to the SiO<sub>2</sub> tunnel layer and Al<sub>2</sub>O<sub>3</sub> blocking layer because the LaMoO film has a lower conduction band than the pure La<sub>2</sub>O<sub>3</sub> film; (iii) the higher electric field in the SiO<sub>2</sub> tunnel layer because the Mo-doped La<sub>2</sub>O<sub>3</sub> film has a higher dielectric constant than the pure La<sub>2</sub>O<sub>3</sub> film. Fig. 3 (b) also shows that the LaMO sample has higher erasing speeds. Owing to the negative conduction-band offset of MoO<sub>3</sub> to the Si substrate, the energy levels of some traps introduced by the Mo doping in the LaMoO film should align with or lie above the valence band of the Si substrate. As a result, during the erasing process, injected holes from the Si substrate can directly recombine with the electrons trapped in the LaMoO film, and thus higher erasing speed can be achieved.<sup>19</sup>

Fig. 4 shows the charge retention characteristics of the two samples. To exclude the effect of internal electric field on charge loss, both samples are initially programmed to a similar memory window (3.98 V for the LaO sample under 14 V for 1 s, and 4.05 V for the LaMO sample under 12 V for 1 ms) before the charge-retention measurement. The deeper trap levels introduced by the Mo doping and the deeper quantum wells provided by the LaMoO CTL both induce better charge retention for the LaMO sample - 85.4% after 10 years, as compared to 77.6% of the LaO sample.

## Conclusion

In this work, the memory characteristics of high-*K* La<sub>2</sub>O<sub>3</sub> with and without Mo doping as charge-trapping layer were investigated. The memory device containing LaMoO film has much larger C-V hysteresis window, much higher P/E speeds, and better charge retention due to deeper-level traps generated by the Mo doping and deeper quantum wells created by the LaMoO film, both desirable for the purpose of charge trapping. Therefore, doping high-*K* dielectrics with suitable high-*K* NCBO material (e.g. MoO<sub>3</sub>) is a promising method to improve the electrical performance of the high-*K* dielectrics used as the CTL of low-voltage memory devices.

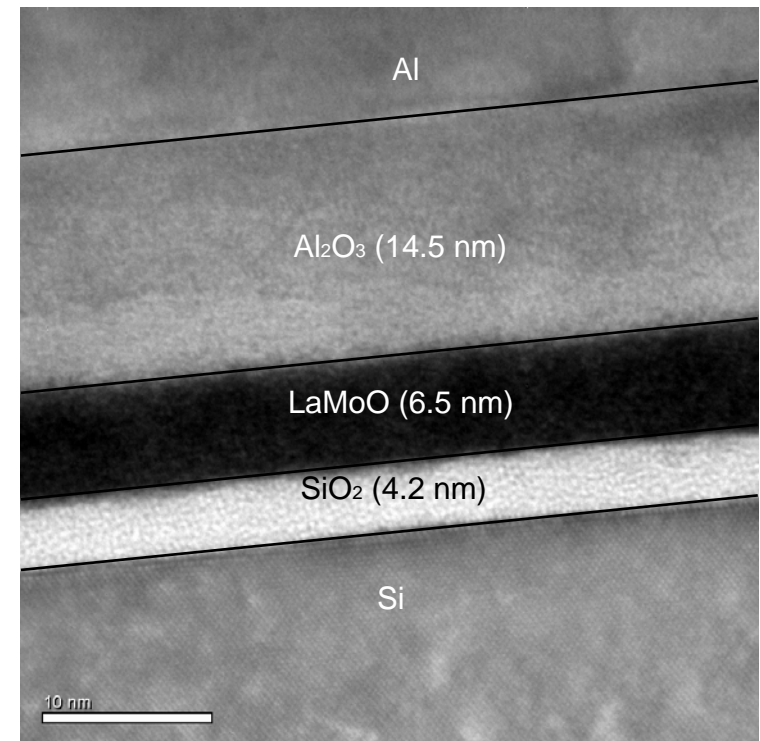
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Figure 1 (a) TEM image of the stacked gate dielectric structure of Al/Al<sub>2</sub>O<sub>3</sub>/LaMoO/SiO<sub>2</sub>/Si after PDA treatment at 900 °C, (b) XPS of La 3d spectrum of the LaO sample and the LaMO sample, and (c) XPS of Mo 3d spectrum of the LaMO sample.



(a)

Figure 2 The C-V hysteresis properties of the LaO sample and the LaMO sample.

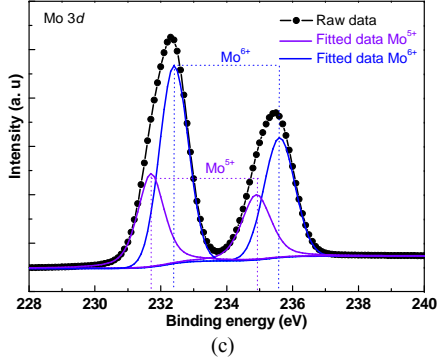
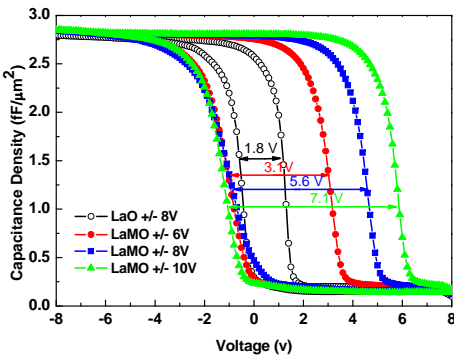
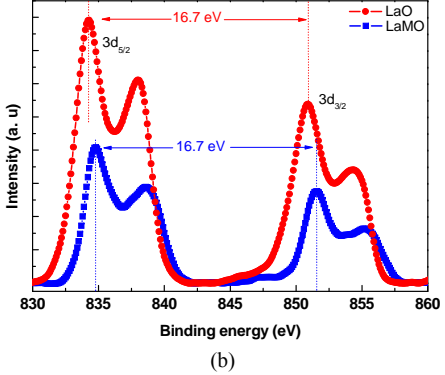
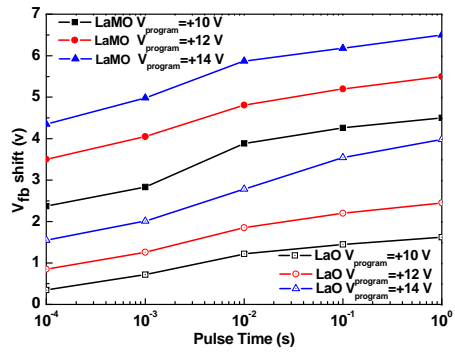
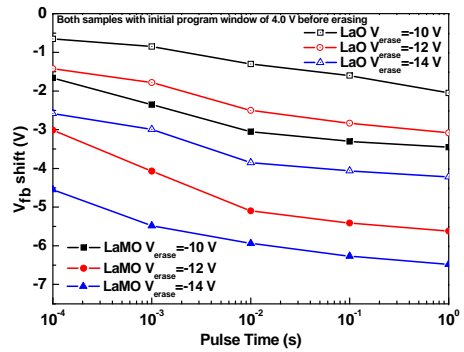


Figure 3(a) Program and (b) erase transient characteristics of the LaO sample and the LaMO sample.



(a)



(b)

Figure 4 The charge retention characteristic of the LaO and LaMO samples.

