Constrained Control of Switched Positive Systems with Discrete and Distributed Delays

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Abstract: This paper is concerned with constrained control of switched positive linear systems with discrete and distributed delays. Our goal is to design a nonnegative controller with a prescribed upper bound, such that the closed-loop system is positive and its state trajectory is located in a boundary whenever the initial condition is within the same boundary. In addition, we investigate the case when the state trajectory never reaches the boundary. It is shown that these problems are solvable via linear programming. Both continuous- and discrete-time cases are investigated in this paper. Finally, several illustrative examples are provided to show the validity of the proposed approach.

Key Words: Constrained control, Positive systems, Switched systems, Time-delay systems

1 Introduction

Positive systems are often utilized to capture physical systems involving quantities which are in nature nonnegative. Unlike general linear systems, the state variables of such systems are confined within a cone instead of a linear space. More specifically, the state and output of positive systems are always constrained in the nonnegative orthant whenever the input and initial condition are nonnegative. Positive systems are frequently encountered in a variety of disciplines, including systems biology, pharmacokinetics [1], ecology [2] and epidemiology. Such systems naturally arise in the modeling of heat exchange, compartmental networks [3] and genetic regulatory networks [4]. It is shown in [5] that some electrical circuits consisting of resistors, capacitors, coils, voltage and current sources can also be modeled by positive systems. Due to their broad applications, great efforts have been devoted to the analysis and synthesis of positive systems in the literature, one may refer to [6-11] and the references therein.

Constrained control, which is also related to controller design with input saturation, has been extensively studied for general linear system, one can refer to [12–14] for some recent developments. This is mainly due to the fact that real plants involve saturations on the input and system states and thus it is of importance to design controllers such that both the input and the state are confined within a prescribed boundary. The study of bounded control for positive system was initiated in [15, 16], where the state-feedback controller synthesis with the requirement of positivity and boundedness was solved based on linear programming. The results were further extended to discrete- and continuous-time positive linear systems with constant discrete delays in [17] and [18], respectively. However, to the best of our knowledge, the above mentioned works only investigate positive systems with constant delays and constrained control of continuoustime positive systems with time-varying delays has not been addressed to date. On the other hand, the synthesis techniques for constrained control of positive systems are quite related to the stabilization problem. Therefore, we recall some results on the stabilization of positive systems.

The state-feedback stabilization problem for positive systems was firstly solved in [19] in terms of linear matrix inequalities. The linear programming based state-feedback controller synthesis method was proposed in [16]. Recently, it is shown in [20] that the output-feedback stabilization problem for positive systems with single input or single output can also be solved directly by linear programming.

Motivated by the above discussion, in this paper, we address the constrained control problem for delayed positive linear systems under arbitrary switching signals. The lemmas [18, Lemmas 3,4] play a key role in the analysis of constrained control for continuous-time positive systems with constant delays. However, the technical proof for these basic lemmas in [18] has to resort to a step-by-step construction of the solution and therefore does not work for the time-varying delay case. In contrast, our approach only relies on the linearity and positivity of the system and therefore is also valid for switched positive systems timevarying discrete and distributed delays. Also note that the methods on constrained control for discrete-time positive systems with delays in [21] rely heavily on mathematical induction and hence are no longer applicable for the analysis of the continuous-time counterpart. In addition, the statefeedback controller proposed in [17, 18] makes use of the delayed state, which is no longer applicable when the magnitudes of the delays are unknown. In this paper, we only design a memoryless controller without delayed feedback, which also works when the delays are unknown. It is worth mentioning that the framework considered in this paper is very general, where the discrete delays are not restricted to be bounded and the switching signal can be arbitrary.

2 Notations

In this section, we introduce some notations on positive systems, which will be frequently used in the following sections. \mathbb{Z} , \mathbb{N} , \mathbb{N}_+ and \mathbb{R} denote the set of all the integers, nonnegative integers, positive integers and real numbers, respectively. All the matrices, if their dimensions are not explicitly stated, are assumed to be compatible for algebraic operations. **1** stands for a column vector with each entry equals 1. diag{ $x_1, x_2, ..., x_n$ } stands for a diagonal matrix

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with diagonal entries being x_1, x_2, \ldots, x_n . A real matrix $A \in \mathbb{R}^{m \times n}$ with all of its entries nonnegative is called nonnegative matrix and is denoted by $A \succeq 0$ and $A \in \mathbb{R}^{m \times n}$. A square matrix $A \in \mathbb{R}^{n \times n}$ with all its off-diagonal entries nonnegative is called Metzler and is denoted by \mathbb{M}^n . For two matrices $A = [a_{ij}], B = [b_{ij}] \in \mathbb{R}^{m \times n}, A \succeq B$ (respectively, $A \succ B$) means that $a_{ij} \ge b_{ij}$ (respectively, $a_{ij} > b_{ij}$) for $i = 1, 2, \ldots, m$ and $j = 1, 2, \ldots, n$.

3 Continuous-time Case

Consider the following continuous-time switched positive linear systems with discrete and distributed delays:

$$\begin{cases} \dot{x}(t) = A_{\sigma(t)}x(t) + A_{d\sigma(t)}x(t - d(t)) \\ + A_{h\sigma(t)} \int_{-h(t)}^{0} x(t + s) ds + B_{\sigma(t)}u(t), \quad t \ge 0, \\ x(s) = \phi(s), \quad s \le 0, \end{cases}$$
(1)

where the map $\sigma(t) : [0,\infty) \to \{1,2,\ldots,N\}$ is a rightcontinuous piecewise-constant function, representing the switching signal with N being the number of subsystems. $x(t) \in \mathbb{R}^n$ is the state vector; $u(t) \in \mathbb{R}^m$ is the control input; A_i , A_{di} and A_{hi} stand for the system matrices of the *i*th subsystem or the *i*th mode of system (1); $\phi(\cdot)$ denotes the initial condition. In this paper, it is assumed that $0 \le h(t) \le h$ $(t \ge 0)$ with h > 0 and the discrete delay $d(t) \ge 0$ is not restricted to be bounded.

Definition 1 [22] *System (1) is called (internally) positive if for all initial condition* $\phi(s) \succeq 0$ ($s \le 0$) *and input* $u(t) \succeq 0$ ($t \ge 0$), *the state trajectory* $x(t) \succeq 0$ *for all* $t \ge 0$.

Lemma 1 System (1) is positive for arbitrary switching signal $\sigma(t)$ and all delays $d(t) \ge 0$, $0 \le h(t) \le h$ if and only if for all i = 1, 2, ..., N, A_i is Metzler, A_{di} , A_{hi} and B_i are all nonnegative.

Proof. (Sufficiency) It suffices to prove that for any T > 0, $x(t) \succeq 0$ for all $t \in [0, T]$. Firstly, it is easy to obtain that all the subsystems are positive by an argument similar to the proof of [23, Theorem II.2]. Then, we can prove the positivity of switched system (1) by mathematical induction. Suppose that the switching sequence over the interval [0, T] is $0 \le t_1 \le t_2 \le \ldots \le t_k \le \ldots \le T$, for any input $u(t) \succeq 0$ and initial condition $\phi(s) \succeq 0$ ($s \le 0$), it is obvious that $x(t) \succeq 0$ for $t \in [0, t_1)$. Due to the continuity of the state trajectory, one has $x(t_1) \succeq 0$. Suppose that $x(t) \succeq 0$ for $t \in [0, t_k]$ and $\sigma(t) = i$ for $t \in [t_k, t_{k+1})$, it can be deduced that $x(t) \succeq 0$ since the *i*th subsystem is positive and its initial condition, it follows that $x(t) \succeq 0$ for $t \in [0, T]$, which completes the sufficiency part.

(Necessity) By considering constant switching signal $\sigma(t) \equiv i \ (t \ge 0)$ and constant delays $d(t) = d, \ h(t) = h$, the necessity holds by [23, Theorem II.2].

The following proposition plays a key role in the formulation of the constrained control problem.

Proposition 1 Suppose that for all i = 1, 2, ..., N, A_i is Metzler, A_{di} and A_{hi} are all nonnegative. Then, given any $\overline{x} \succ 0$, for any initial condition $0 \preceq \phi(s) \preceq \overline{x}$ ($s \le 0$), the state

trajectory of system (1) with u(t) = 0 satisfies that $x(t) \leq \overline{x}$ ($t \geq 0$) for arbitrary switching signal $\sigma(t)$ and all delays $d(t) \geq 0, \ 0 \leq h(t) \leq h$ if and only if $(A_i + A_{di} + hA_{hi})\overline{x} \leq 0$ for all i = 1, 2, ..., N.

Proof. (Sufficiency) Define $e(t) \triangleq \overline{x} - x(t)$, then e(t) satisfies the following equation:

$$\dot{e}(t) = A_{\sigma(t)}e(t) + A_{d\sigma(t)}e(t - d(t)) + A_{h\sigma(t)} \int_{-h(t)}^{0} e(t + s) ds - (A_{\sigma(t)} + A_{d\sigma(t)} + h(t)A_{h\sigma(t)})\overline{x}.$$
(2)

Note that the initial condition $e(s) \succeq 0$ since $\phi(s) \preceq \overline{x}$ for $s \leq 0$. Also note that $(A_{\sigma(t)} + A_{d\sigma(t)} + h(t)A_{h\sigma(t)})\overline{x} \preceq (A_{\sigma(t)} + A_{d\sigma(t)} + hA_{h\sigma(t)})\overline{x} \preceq 0$, since both $A_{h\sigma(t)}$ and \overline{x} are nonnegative. By regarding $-(A_{\sigma(t)} + A_{d\sigma(t)} + h(t)A_{h\sigma(t)})\overline{x}$ as a nonnegative input, it follows that $e(t) \succeq 0$ for all $t \ge 0$ due to the positivity of the error system (2). Therefore, $x(t) \preceq \overline{x}$ for all $t \ge 0$, which completes the sufficiency part.

(Necessity) Fix $\sigma(t) = i$, d(t) = d and h(t) = h ($t \ge 0$), then by noting that $0 \succeq \dot{x}(0+) = (A + A_{di} + hA_{hi})\bar{x}$, the necessity directly follows.

Different from Proposition 1, the following proposition ensures that the state trajectory never hits the boundary \overline{x} .

Proposition 2 Suppose that for all i = 1, 2, ..., N, A_i is Metzler, A_{di} and A_{hi} are all nonnegative. Also assume that there exists $\overline{x} \succ 0$, such that $(A_i + A_{di} + hA_{hi})\overline{x} \prec 0$ for all i = 1, 2, ..., N. Then, for arbitrary switching signal $\sigma(t)$, any delays $d(t) \ge 0$, $0 \le h(t) \le h$ and any initial condition $0 \le \phi(s) \le \overline{x}$ ($s \le 0$), the state trajectory of system (1) with u(t) = 0 satisfies that $x(t) \prec \overline{x}$ for all t > 0.

Proof. Define $e(t) \triangleq \overline{x} - x(t)$, then e(t) satisfies equation (2). Since A_i (i = 1, 2, ..., N) are Metzler matrices, one can find a sufficiently large $\mu > 0$, such that $\mu I + A_i \succeq 0$ for all i = 1, 2, ..., N. Denote $U(t) \triangleq (\mu I + A_{\sigma(t)})e(t) + A_{d\sigma(t)}e(t - d(t)) + A_{h\sigma(t)} \int_{-h(t)}^{0} e(t + s) ds$ and we can rewrite the error system (2) as

$$\dot{e}(t) = -\mu e(t) + U(t) - (A_{\sigma(t)} + A_{d\sigma(t)} + h(t)A_{h\sigma(t)})\overline{x}.$$

Then, it can be readily obtained that

$$e(t) = e^{-\mu t} e(0) + \int_0^t e^{-\mu(t-\tau)} U(\tau) d\tau$$
$$- \int_0^t e^{-\mu(t-\tau)} (A_{\sigma(\tau)} + A_{d\sigma(\tau)} + h(\tau) A_{h\sigma(\tau)}) \overline{x} d\tau.$$

Note that $e(t) \succeq 0$ for $t \ge 0$ by Proposition 1 and hence $U(t) \succeq 0$ for $t \ge 0$. Since $(A_{\sigma(t)} + A_{d\sigma(t)} + h(t)A_{h\sigma(t)})\overline{x} \preceq (A_{\sigma(t)} + A_{d\sigma(t)} + hA_{h\sigma(t)})\overline{x} \prec 0$ for all $t \ge 0$, it follows that $\int_0^t e^{-\mu(t-\tau)} (A_{\sigma(\tau)} + A_{d\sigma(\tau)} + h(\tau)A_{h\sigma(\tau)})\overline{x}d\tau \prec 0$ for t > 0 and thus $e(t) \succ 0$ for all t > 0. This completes the proof. \Box

In the sequel, we focus on system (1) with a memoryless state-feedback control input u(t) = Kx(t). Given an upper bound $\overline{u} \succ 0$ on the controller, our goal is to find $\overline{x} \succ 0$ as well as a controller gain matrix K, such that the closed-loop system (1) under control input u(t) is positive and satisfies that for any initial condition $0 \preceq \phi(s) \preceq \overline{x}$ ($s \le 0$), it holds that the state trajectory $0 \preceq x(t) \preceq \overline{x}$ (or $0 \preceq x(t) \prec \overline{x}$) and the

input $0 \leq u(t) \leq \overline{u}$ for all t > 0. This synthesis problem is addressed in the following theorems.

Theorem 1 Assume that $A_d, A_h \in \overline{\mathbb{R}}_+^{n \times n}$. Given $u \succ 0$, for arbitrary switching signal $\sigma(t)$ and all delays $d(t) \ge 0$, $0 \le h(t) \le h$, there exist $\overline{x} \succ 0$ and a feedback gain matrix K, such that system (1) under control input u(t) = Kx(t) satisfies that $0 \le x(t) \le \overline{x}$ and $0 \le u(t) \le \overline{u}$ for all t > 0 whenever $0 \le \phi(s) \le \overline{x}$ ($s \le 0$) if and only if there exist a diagonal matrix X with positive diagonal entries and matrix $Z \succeq 0$, such that the following linear program is feasible:

$$A_{i}X + B_{i}Z \in \mathbb{M}^{n},$$

$$(A_{i} + A_{di} + hA_{hi})X\mathbf{1} + B_{i}Z\mathbf{1} \leq 0,$$

$$Z\mathbf{1} \leq \overline{u}, \quad i = 1, 2, \dots, N.$$
(3)

Then, \overline{x} and K can be obtained as $\overline{x} = X\mathbf{1}$ and $K = ZX^{-1}$, respectively.

Proof. (Sufficiency) Denote $\overline{x} = X\mathbf{1}$ and $K = ZX^{-1}$, it follows from the first condition in (3) that $(A_i + B_iK)X$ is Metzler for i = 1, 2, ..., N. Hence, $A_i + B_iK$ is Metzler since X is a diagonal matrix with positive diagonal entries, which implies the positivity of system (1) under control input u(t). Constraint $(A_i + A_{di} + hA_{hi})X\mathbf{1} + B_iZ\mathbf{1} \leq 0$ is equivalent to $(A_i + B_iK + A_{di} + hA_{hi})\overline{x} \leq 0$, which results in $0 \leq x(t) \leq \overline{x}$ for $t \geq 0$ by Proposition 1. Note that constraints $Z\mathbf{1} \leq \overline{u}$ and $Z \succeq 0$ imply that $K\overline{x} \leq \overline{u}$ and $K \succeq 0$, respectively, which further yields that $0 \leq u(t) = Kx(t) \leq K\overline{x} \leq \overline{u}$ since $0 \leq x(t) \leq \overline{x}$. This completes the sufficiency part.

(Necessity) Let us fix $\sigma(t) = i$ and consider constant delays d(t) = d and h(t) = h. Note that by Proposition 1, $0 \leq x(t) \leq \overline{x}$ implies that $(A_i + B_iK + A_{di} + hA_{hi})\overline{x} \leq 0$. Also note that $u(0) = K\overline{x} \leq \overline{u}$. Denote $X = \text{diag}\{\overline{x_1}, \overline{x_2}, \dots, \overline{x_n}\}$ where $\overline{x_i}$ is the *i*th entry of vector \overline{x} . Then, it follows that $(A_i + B_iK + A_{di} + hA_{hi})X1 \leq 0$ and $u(0) = KX1 \leq \overline{u}$. In addition, $K \succeq 0$ can be inferred from $u(0) = Kx(0) \succeq 0$ with arbitrary $0 \leq x(0) \leq \overline{x}$. The positivity of system (1) under control input u(t) reveals that $A_i + B_iK$ is Metzler for $i = 1, 2, \dots, N$. Since X is a diagonal matrix with positive diagonal entries, it is obvious that $(A_i + B_iK)X$ is Metzler for $i = 1, 2, \dots, N$. Denoting Z = KX and summarizing the above conditions, we immediately arrive at constraints (3). \Box

Theorem 2 Assume that $A_d, A_h \in \overline{\mathbb{R}}_+^{n \times n}$. Given $\overline{u} \succ 0$, for arbitrary switching signal $\sigma(t)$ and all delays $d(t) \ge 0$, $0 \le h(t) \le h$, there exist $\overline{x} \succ 0$ and a feedback gain matrix K, such that system (1) under control input u(t) = Kx(t)satisfies that $0 \le x(t) \prec \overline{x}$ and $0 \le u(t) \le \overline{u}$ for all t > 0whenever $0 \le \phi(s) \le \overline{x}$ ($s \le 0$) if there exist a diagonal matrix X with positive diagonal entries and matrix $Z \succeq 0$, such that the following linear program is feasible:

$$A_i X + B_i Z \in \mathbb{M}^n,$$

$$(A_i + A_{di} + hA_{hi}) X \mathbf{1} + B_i Z \mathbf{1} \prec 0,$$

$$Z \mathbf{1} \preceq \overline{u}, \quad i = 1, 2, \dots, N.$$
(4)

Then, \overline{x} and K can be obtained as $\overline{x} = X\mathbf{1}$ and $K = ZX^{-1}$, respectively.

Theorem 2 can be proved by following a line similar to that of Theorem 1.

4 Discrete-time Case

In this section, let us consider the following discretetime switched positive linear systems with discrete and distributed delays:

$$\begin{cases} x(k+1) = A_{\sigma(k)}x(k) + A_{d\sigma(k)}x(k-d(k)) \\ + A_{h\sigma(k)}\sum_{s=1}^{h(k)} x(k-s) + B_{\sigma(k)}u(k), & k \in \mathbb{N}, \\ x(s) = \phi(s), & s \le 0, s \in \mathbb{Z}, \end{cases}$$
(5)

where the map $\sigma(k) : \mathbb{N} \to \{1, 2, ..., N\}$ stands for the switching signal with *N* representing the number of subsystems. $x(k) \in \mathbb{R}^n$ denotes the state vector; $u(k) \in \mathbb{R}^m$ is the control input; $\phi(\cdot)$ is the initial condition. In this section, we assume that for all $k \in \mathbb{N}$, $h(k) \in \mathbb{N}_+$ and $1 \le h(k) \le h$ with $h \in \mathbb{N}_+$. The delay $d(k) \ge 0$ can be unbounded. This model can be regarded as a discretized model of system (1).

Definition 2 [22] *System (5) is called (internally) positive if for all initial condition* $\phi(s) \succeq 0$ ($s \le 0, s \in \mathbb{Z}$) *and input* $u(k) \succeq 0$ ($\forall k \in \mathbb{N}$), the state trajectory $x(k) \succeq 0$ for all $k \in \mathbb{N}$.

Lemma 2 System (5) is positive for arbitrary switching signal $\sigma(k)$ and all delays $d(k) \in \mathbb{N}$, $1 \le h(k) \le h$ if and only if for all i = 1, 2, ..., N, A_i , A_{di} , A_{hi} and B_i are all nonnegative.

Lemma 2 can be easily proved by mathematical induction, the details are omitted here. In order to exploit the constrained control problem for discrete-time system (5), the following two propositions are needed.

Proposition 3 Suppose that for all i = 1, 2, ..., N, A_i , A_{di} and A_{hi} are all nonnegative. Then, given any $\overline{x} \succ 0$, for any initial condition $0 \preceq \phi(s) \preceq \overline{x}$ ($s \le 0, s \in \mathbb{Z}$), the state trajectory of system (5) with u(t) = 0 satisfies that $x(k) \preceq \overline{x}$ ($k \in \mathbb{N}$) for arbitrary switching signal $\sigma(k)$ and all delays $d(k) \in \mathbb{N}$, $1 \le h(k) \le h$ if and only if $(A_i + A_{di} + hA_{hi} - I)\overline{x} \preceq 0$ for all i = 1, 2, ..., N.

Proof. Let $e(k) \triangleq \overline{x} - x(k)$, and e(k) satisfies the following equation:

$$e(k+1) = A_{\sigma(k)}e(k) + A_{d\sigma(k)}e(k-d(k)) + A_{h\sigma(k)}\sum_{s=1}^{h(k)} e(k-s) - (A_{\sigma(k)} + A_{d\sigma(k)} + h(k)A_{h\sigma(k)} - I)\overline{x}.$$
(6)

The rest of the proof is similar to that of Proposition 1 and is therefore omitted here. $\hfill \Box$

Proposition 4 Suppose that A_i , A_{di} and A_{hi} are all nonnegative for all i = 1, 2, ..., N. Then, given any $\overline{x} \succ 0$, for any initial condition $0 \preceq \phi(s) \preceq \overline{x}$ ($s \le 0, s \in \mathbb{Z}$), the state trajectory of system (5) with u(t) = 0 satisfies that $0 \preceq x(k) \prec \overline{x}$ ($k \in \mathbb{N}_+$) for arbitrary switching signal $\sigma(k)$ and all delays $d(k) \in \mathbb{N}$, $1 \le h(k) \le h$ if and only if $(A_i + A_{di} + hA_{hi} - I)\overline{x} \prec 0$ for all i = 1, 2, ..., N.

Proof. (Sufficiency) Let $e(k) \triangleq \overline{x} - x(k)$, and e(k) satisfies equation (6). From Proposition 3, it follows that $e(k) \succeq 0$

for all $k \in \mathbb{N}$. Then it is obvious that $e(k+1) \succeq -(A_{\sigma(k)} + A_{d\sigma(k)} + h(k)A_{h\sigma(k)} - I)\overline{x} \succeq -(A_{\sigma(k)} + A_{d\sigma(k)} + hA_{h\sigma(k)} - I)\overline{x} \succ 0$ for all $k \in \mathbb{N}$, which completes the sufficiency part.

(Necessity) Let $\sigma(t) \equiv i$, d(k) = d and h(k) = h, then we have $x(1) = (A_i + A_{di} + hA_{hi})\overline{x} \prec \overline{x}$, which implies that $(A_i + A_{di} + hA_{hi} - I)\overline{x} \prec 0$. This completes the necessity part. \Box

Remark 1 It is worth mentioning that unlike the continuous-time counterpart considered in Proposition 2, the condition $(A_i + A_{di} + hA_{hi} - I)\overline{x} \prec 0$ is necessary for the discrete-time case.

In the following, we aim at designing a state-feedback controller of the form u(k) = Kx(k) for discrete-time switched system (5). Given a prescribed upper bound $\overline{u} \succ 0$ on the controller, we aim to find a column vector $\overline{x} \succ 0$ together with a feedback gain matrix K, such that system (5) under control input u(t) is positive and satisfies that for any initial condition $0 \leq \phi(s) \leq \overline{x}$, $0 \leq x(k) \leq \overline{x}$ (or $0 \leq x(k) \prec \overline{x}$) and $0 \leq u(k) \leq \overline{u}$ holds for all $k \in \mathbb{N}_+$. This problem is shown to be solvable via linear programming in the following theorem.

Theorem 3 Assume that $A_d, A_h \in \overline{\mathbb{R}}_+^{n \times n}$. Given $\overline{u} \succ 0$, for arbitrary switching signal $\sigma(k)$ and all delays $d(k) \in \mathbb{N}$, $1 \leq h(k) \leq h$, there exist $\overline{x} \succ 0$ and a feedback gain matrix K, such that system (5) under control input u(k) = Kx(k)satisfies that $0 \leq x(k) \leq \overline{x}$ (respectively, $0 \leq x(k) < \overline{x}$) and $0 \leq u(k) \leq \overline{u}$ for $k \in \mathbb{N}_+$ whenever the initial condition satisfies that $0 \leq \phi(s) \leq \overline{x}$ ($s \leq 0, s \in \mathbb{Z}$) if and only if there exist a diagonal matrix X with positive diagonal entries and matrix $Z \succeq 0$, such that the following linear program is feasible:

$$A_{i}X + B_{i}Z \succeq 0,$$

$$(A_{i} + A_{di} + hA_{hi} - I)X\mathbf{1} + B_{i}Z\mathbf{1} \preceq 0 \text{ (respectively, } \prec 0),$$

$$Z\mathbf{1} \preceq \overline{u}, \quad i = 1, 2, \dots, N.$$
(7)

Then, \overline{x} and K can be obtained as $\overline{x} = X\mathbf{1}$ and $K = ZX^{-1}$, respectively.

5 Illustrative Examples

Example 1 Consider discrete-time switched system (5) with two subsystems whose system matrices are given as follows:

$$A_{1} = \begin{bmatrix} 0.2 & 0.2 & -0.1 \\ -0.1 & -0.4 & -0.1 \\ -0.1 & 0.1 & 0.3 \end{bmatrix}, B_{1} = \begin{bmatrix} -0.7 & 0.6 \\ 0.4 & 0.4 \\ 0.2 & 0 \end{bmatrix},$$
$$A_{d1} = \begin{bmatrix} 0.1 & 0.2 & 0 \\ 0 & 0.3 & 0 \\ 0.1 & 0.1 & 0.2 \end{bmatrix}, A_{h1} = \begin{bmatrix} 0.02 & 0.01 & 0 \\ 0.02 & 0 & 0.05 \\ 0.02 & 0.01 & 0 \end{bmatrix},$$
$$A_{2} = \begin{bmatrix} 0.3 & -0.2 & 0.1 \\ 0.1 & -0.5 & 0.05 \\ 0.1 & 0 & 0.2 \end{bmatrix}, B_{2} = \begin{bmatrix} -1 & 0.8 \\ -0.3 & 0.6 \\ 0.3 & -0.1 \end{bmatrix},$$
$$A_{d2} = \begin{bmatrix} 0.05 & 0 & 0.1 \\ 0.1 & 0 & 0 \\ 0 & 0 & 0.3 \end{bmatrix}, A_{h2} = \begin{bmatrix} 0 & 0 & 0.05 \\ 0 & 0.07 & 0.03 \\ 0 & 0 & 0.05 \end{bmatrix}.$$

The delays are chosen as $d(k) = \sqrt{k}$ and $h(k) = |4|\sin(k)|| + 1$ where $|\cdot|$ is the floor function. Given

 $\overline{u} = \begin{bmatrix} 30 & 40 \end{bmatrix}^T$ and h = 4, we aim to design a feedback gain K and find a column vector $\overline{x} \succ 0$, such that system (5) under control input u(t) is positive and satisfies that $0 \leq x(k) \prec \overline{x}$ and $0 \leq u(k) \leq \overline{u}$ holds for all $k \in \mathbb{N}_+$, whenever the initial condition $0 \leq \phi(s) \leq \overline{x}$. Inspired by [15], we also try to maximize the boundary, that is, max $\mathbf{1}^T \overline{x}$. By $\max_{Z,X} \mathbf{1}^T X \mathbf{1}$ subject to the constraints in (7), we can obtain a feedback gain K as

$$K = \left[\begin{array}{ccc} 0.5000 & 0.3334 & 0.0385 \\ 0.2501 & 1.0000 & 0.2116 \end{array} \right]$$

and \overline{x} can be attained as $\overline{x} = [26.9692, 25.7866, 35.2903]^T$. Starting from constant initial condition $\phi(s) \equiv \overline{x}$, the trajectory of system (5) under control input u(k) and randomly generated switching signal is plotted in Fig. 2. The switching sequence is depicted in Fig. 1. Note that for any other initial condition $0 \leq \phi(s) \leq \overline{x}$ ($s \leq 0, s \in \mathbb{Z}$), the trajectory must be below this due to the positivity of the system. It can be observed that $0 \leq x(k) < \overline{x}$ for $k \in \mathbb{N}_+$ from Fig. 2 and it can be verified that $u(k) = Kx(k) \leq K\overline{x} = [23.4386 \quad 39.9982]^T \leq \overline{u}$.

Example 2 Let us consider continuous-time switched system (1) with two subsystems represented by the following matrices:

$$\begin{split} A_1 &= \begin{bmatrix} -0.5 & 0.1 & -0.5 \\ 0.7 & -1 & 0.5 \\ -0.1 & 0.1 & -3 \end{bmatrix}, B_1 = \begin{bmatrix} 0 & 0.2 \\ -1 & -0.1 \\ -0.2 & 0.45 \end{bmatrix}, \\ A_{d1} &= \begin{bmatrix} 0.1 & 0 & 0.1 \\ 0.1 & 0 & 0 \\ 0 & 0 & 0.4 \end{bmatrix}, A_{h1} = \begin{bmatrix} 0.02 & 0.01 & 0 \\ 0 & 0.02 & 0.03 \\ 0.04 & 0 & 0.03 \end{bmatrix}, \\ A_2 &= \begin{bmatrix} -1 & -0.5 & 1.6 \\ 0.4 & 0 & 0.8 \\ -0.06 & -0.1 & -2.5 \end{bmatrix}, B_2 = \begin{bmatrix} 0.1 & 0.4 \\ -1.5 & 0.5 \\ -0.1 & 0.2 \end{bmatrix}, \\ A_{d2} &= \begin{bmatrix} 0.1 & 0.2 & 0.5 \\ 0 & 0 & 0.1 \\ 0.1 & 0.3 & 0.2 \end{bmatrix}, A_{h2} = \begin{bmatrix} 0 & 0 & 0.04 \\ 0.02 & 0 & 0.06 \\ 0.04 & 0 & 0.05 \end{bmatrix}. \end{split}$$

The delays are chosen as $d(t) = 0.1t(1 + \sin t)$ and $h(t) = 2 + \cos t$. The boundary for the input is given as $\overline{u} = [30 \ 100]^T$ and h = 3. One can easily check that the open-loop subsystem 2 without delays is neither positive nor stable. By $\max_{Z,X} \mathbf{1}^T X \mathbf{1}$ subject to the constraints in (4), a feedback gain K can be attained as

$$K = \left[\begin{array}{ccc} 0.1143 & 3.0641 & 0.2499 \\ 0.3571 & 2.0321 & 2.5001 \end{array} \right]$$

and \overline{x} is given by $\overline{x} = [35.4268 \ 7.9695 \ 6.1280]^T$. Starting from initial condition $\phi(s) \equiv \overline{x}$, the trajectory of system (1) under control input u(t) and randomly generated switching signal is depicted in Fig. 4. The corresponding switching signal is given in Fig. 3. One can observe that $0 \leq x(t) < \overline{x}$ for t > 0 from Fig. 4. It can also be verified that $u(t) = Kx(t) \leq K\overline{x} = [29.9992 \ 44.1677]^T \leq \overline{u}$.

6 Conclusions

In this paper, constrained control of switched positive systems with delays has been studied. Based on linear programming, we have designed a state-feedback controller, which is nonnegative and within a prescribed bound, such that the closed-loop system is positive and its state trajectory lies in a boundary when the initial condition is located in the same boundary. This paper can be regarded as extensions of existing works. Different from these works, the method only need to make use of the linearity and positivity of the system and therefore is still applicable when the time delays are time-varying. As a future research direction, constrained control of switched positive systems via output feedback would be an interesting yet challenging problem.

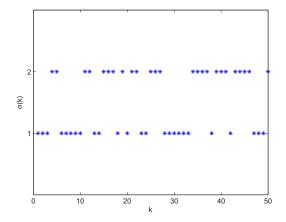


Fig. 1: Switching signal for discrete-time system (5).

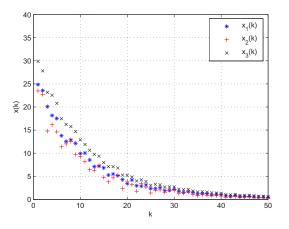


Fig. 2: State trajectory of system (5) under input u(k) = Kx(k).

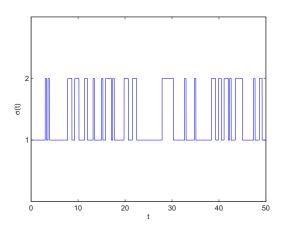


Fig. 3: Switching signal for continuous-time system (1).

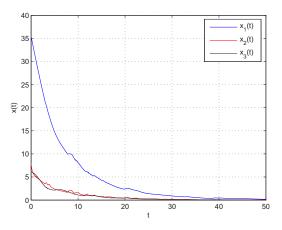


Fig. 4: State trajectory of system (1) under input u(t) = Kx(t).

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