### LAPPED ORTHOGONAL TRANSFORM WITH INTEGER COEFFICIENTS

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#### ABSTRACT

In this paper, a new 8-channel integer-valued lapped transform, called the Integer Lapped Orthogonal Transform (ILOT), is proposed. The new transform can be implemented using simple integer arithmetic and has very low implementation complexity. Using 5-bits to represent its transform kernel, it was found that good coding gain and stop band attenuation, similar to the LOT, could be achieved. The multiplier-less fast ILOT requires only 106 additions and 42 shifts and can be implemented in 32-bit integer arithmetic [13].

### 1. INTRODUCTION

Perfect Reconstruction (PR) critically decimated quadrature mirror filter banks (QMFB) have many important applications in speech, audio and image processing. The theory and design of M-channel PR maximally decimated uniform filter bank has been extensively studied [7]. The cosine modulated filter banks (CMFB) [9] and the lapped transforms [2], [4], [6] are two efficient classes of filter banks with low implementation complexity and good performance. The lapped orthogonal transform (LOT) is an M-channel linear-phase PR filter bank with filter length 2M. The LOT has slightly higher arithmetic complexity than the DCT but the coding gain is significantly higher with much less blocking artifacts. Recently, there is increasing interest in designing filter banks with low implementation complexity. Approaches based on the sum of power of two (SOPOT) coefficients [3] or integer coefficients [12] have been proposed. Due to the use of SOPOT, coefficient multiplications can be implemented with simple shifts and additions. In [12], a subspace approach was proposed to design CMFB with integer coefficient prototype filter. Since integer coefficient filter banks require only integer arithmetic (additions and possibly multiplications), the implementation of the filter banks are greatly simplified. Also, if sufficient word length is used to represent the intermediate data, round off error can completely be eliminated.

In this paper, a new lapped orthogonal transform with integer coefficients, called the integer LOT (ILOT) is developed. It can be implemented with simple integer arithmetic and possess very fast implementation algorithm. A multiplier-less implementation has also been developed which requires a total of 106 additions and 42 shifts [13]. Using 5-bits to represent the transform kernel, the ILOT is found to have similar coding gain and image coding performance as the LOT. The paper is organized

as follows. Section 2 reviews the design of ICT, which was originally proposed by Cham [1]. Construction of the Integer Lapped Orthogonal Transform (ILOT) is described in section 3. The design and the performance of the ILOT are given in section 4. Finally, the results are summarized in section 5, the conclusion.

### 2. INTEGER SINUSOIDAL TRANSFORMS

According to Wang [10], there are four types of DCTs and DSTs (type I to IV). DCT-II and DCT-III are the common discrete cosine transform (DCT) and the inverse DCT (IDCT). In this paper, the integer version of the DCT-II (DST-II) and the DCT-IV (DST-IV) are developed to construct the integer lapped orthogonal transform (ILOT). The definition of the DCT-II, DCT-IV, DST-II and DST-IV are given as follows,

$$\begin{bmatrix} C_{M}^{"} \end{bmatrix} = \sqrt{2 / M} \left\{ \varepsilon_{k} \cos(k(n+1/2)\pi / M) \right\},$$

$$k, n = 0, 1, \dots, M - 1.$$

$$\begin{bmatrix} C_{M}^{"} \end{bmatrix} = \sqrt{2 / M} \left\{ \varepsilon_{k} \cos((k+1/2)(n+1/2)\pi / M) \right\},$$

k, n = 0, 1, ..., M - 1.

 $\begin{bmatrix} S_{M}^{n} \end{bmatrix} = \sqrt{2 / M} \left\{ \varepsilon_{k} \sin(k(n - 1/2)\pi / M) \right\},$  k, n = 1, ..., M.  $\begin{bmatrix} S_{M}^{n} \end{bmatrix} = \sqrt{2 / M} \left\{ \varepsilon_{k} \sin((k + 1/2)(n + 1/2)\pi / M) \right\},$  k, n = 0, 1, ..., M - 1.

Where 
$$\varepsilon_i = \begin{cases} 1/\sqrt{2} & i = 0 \text{ or } M\\ 1 & otherwise \end{cases}$$
 (1)

Here  $[A]_{i,j}$  represents the (i,j)-th element of matrix A. The subscript and the superscript of C and S denote, respectively, the length and the type of the transform.

The type-II integer cosine transform (ICT),  $T_M^{C-H}$ , proposed in [1] has the following matrix form.

$$P_{H}^{C-H} = K_{H}^{C-H} \cdot T_{H}^{C-H} , \qquad (2)$$

where  $K_M^{c-H}$  is a real diagonal matrix and  $P_M^{c-H}$  is an orthogonal matrix. The superscript and subscript denote, respectively, the type and the length of the transform. For example, C-H means the type II cosine transform and so on. Using the concept of dyadic symmetry, Cham [1] constructed an order-8 ICT of the following form,

$$T_{s}^{c-u} = \begin{bmatrix} k & k & k & k & k & k & k & k & k \\ a & b & c & d & -d & -c & -b & -a \\ e & f & -f & -e & -e & -f & f & e \\ b & -d & -a & -c & c & a & d & -b \\ k & -k & -k & k & k & -k & -k & k \\ c & -a & d & b & -b & -d & a & -c \\ f & -e & e & -f & -f & e & -e & f \\ d & -c & b & -a & a & -b & c & -d \end{bmatrix}$$
(3)

To satisfy the orthogonality properties, a, b, c, and d must satisfy the following condition,

$$a \cdot b = a \cdot c + b \cdot d + c \cdot d . \tag{4}$$

Also, in order to make the basis vectors of the ICT resemble those of the DCT-II, a, b, c, d, e, and f must satisfy the conditions,

$$a \ge b \ge c \ge d$$
 and  $e \ge f$  (5)

In [1], the constants a, b, c, d, e, f, and k are chosen to be 8-bit integers. The (i,j)-th entry of matrix  $K_M^{c-u}$ ,  $k_i$ , is used to normalize the i-th row of  $T_M^{c-u}$  so that  $P_M^{c-u}$  is orthogonal. If  $T_i$  is the  $L_2$ -norm of the i-th row of  $T_M^{c-u}$ , then

$$k_i = |1/T_i|. (6)$$

An exhaustive search is then performed to maximize the coding gain for common AR(1) process. The multiplication with  $k_i$ 's are usually absorbed into the quantization process to reduce the complexity. The inverse of the ICT is given by.

$$(P_{M}^{C-H})^{-1} = (P_{M}^{C-H})^{T} = (K_{M}^{C-H} \cdot T_{H}^{C-H})^{T}$$
$$= (T_{M}^{C-H})^{T} (K_{M}^{C-H})^{T}$$
(7)

The scaling  $(K_M^{c-u})^T$  can also be absorbed into the dequantization process.

#### 3. INTEGER LOT (ILOT)

The transform matrix of an M-channel LOT is given by [2],

$$P_{M}^{LOT} = P_{M} \begin{bmatrix} I_{M\Omega} & \theta_{N\Omega} \\ \theta_{M\Omega} & (C_{M\Omega}^{\Pi} S_{M\Omega}^{N})^{T} \end{bmatrix} \cdot \begin{bmatrix} D_{\epsilon} - D_{o} & J_{M\Omega} (D_{\epsilon} - D_{o}) \\ D_{\epsilon} - D_{o} & -J_{M\Omega} (D_{\epsilon} - D_{o}) \end{bmatrix}, \quad (8)$$

where D, and  $D_o$  are  $(M/2 \times M)$  matrices containing the even and odd basis functions of DCT-II, respectively.  $P_M$  is a permutation matrix which permutes the k th and the (k+M/2) th rows to the 2k th and the (2k+1) th  $(k=0,\cdots,M/2-1)$  rows, respectively. Fig. 1 shows the flow graph of an 8-channel LOT. It is natural to consider replacing the 8-point DCT in the LOT by  $P_k^{c-u}$  to construct an integer LOT. Unfortunately, the scaling matrix,  $K_k^{c-n}$ , is real-valued. Therefore, it is necessary to perform real multiplications immediately after  $T_k^{c-u}$  before subsequent additions in the LOT can be performed. A method to avoid this problem is to design an integer cosine transform with all the scalings being

identical. In this case, the scaling constant can be moved to the end of the transformation and absorbed in the quantization process. The remaining problem is to find similar integer transform for the 4-point DCT-II and the 4-point DST-IV. Using the same concept in [1], we propose the following form for the order-4 type-II integer cosine transform,

$$T_{4}^{C-R} = \begin{bmatrix} \ell & \ell & \ell & \ell \\ a_{1} & b_{1} & -b_{1} & -a_{1} \\ \ell & -\ell & -\ell & \ell \\ b_{1} & -a_{1} & a_{1} & -b_{1} \end{bmatrix}, \tag{9}$$

where  $a_1$ ,  $b_1$ , and  $\ell$  are integers to be determined. Like  $T_8^{C-H}$ ,  $a_1$  and  $b_1$  have to satisfy,

$$a_1 \ge b_1 \,. \tag{10}$$

Similarly, the order-4 type-IV integer sine transform is defined as follows,

$$T_4^{S-N} = \begin{bmatrix} a_2 & b_1 & c_1 & d_1 \\ b_2 & d_1 & a_1 & -c_2 \\ c_1 & a_1 & -d_2 & b_1 \\ d_2 & -c_2 & b_1 & -a_2 \end{bmatrix}. \tag{11}$$

To satisfy the orthogonal properties,  $a_2, b_1, c_2, d_2$  must satisfy the following condition,

$$c_1 \cdot d_2 = a_2 \cdot b_1 + b_2 \cdot d_2 + c_2 \cdot a_2$$
. (12)

In addition, they have to satisfy the following condition to resemble the DCT-IV,

$$a_2 \ge b_2 \ge c_2 \ge d_1 \tag{13}$$

For simplicity the scaling constants for the matrix product,  $(T_{\bullet}^{c-n}T_{\bullet}^{s-n})^T$ , are designed to be identical. Finally, the transform matrix of the order-8 ILOT is obtained as follows.

$$P_{3}^{\mu,or} = P_{\mu} \begin{bmatrix} k_{0}I_{4} & \theta_{4} \\ \theta_{4} & k_{1}I_{4} \end{bmatrix} \begin{bmatrix} I_{4} & \theta_{4} \\ \theta_{4} & (T_{4}^{C-\mu}T_{4}^{S-\mu\nu})^{T} \end{bmatrix}$$

$$\begin{bmatrix} \tilde{D}_{c} - \tilde{D}_{o} & J_{4}(\tilde{D}_{c} - \tilde{D}_{o}) \\ \tilde{D}_{c} - \tilde{D}_{o} & -J_{c}(\tilde{D}_{c} - \tilde{D}_{c}) \end{bmatrix}. \tag{14}$$

where  $\tilde{D}_{s}$  and  $\tilde{D}_{s}$  are (4×8) matrices containing the even and odd basis functions of  $T_{s}^{c-\mu}$ , respectively. The scaling constant for  $T_{s}^{c-\mu}$  is  $k_{0}$ . For odd-indexed outputs, the scaling of  $T_{s}^{c-\mu}$  and  $(T_{s}^{c-\mu}T_{s}^{s-\nu})^{T}$  are merged together to form  $k_{1}$ . These multiplications can readily be absorbed into the quantization process. Fig. 2 shows the flow graph of the 8-channel integer LOT. The ILOT is parameterized by a set of integers and is referred to as

*ILOT* ( $a,b,c,d,e,f,k,a_1,b_1,\ell,a_2,b_2,c_2,d_2$ ). As an example, *ILOT* (24,20,12,6,23,7,17,17,13,3,6,10,12) refers to the 8-channel length-16 *ILOT* with a=24, b=20, c=12, d=6, e=23, f=7, k=17,  $a_1=17$ ,  $b_1=7$ , l=13,  $a_1=3$ ,  $b_2=6$ , c=10 and d=12.

## 4. DESIGN OF THE ILOT

We now proceed to the design of the ILOT. An important issue in transform and subband coding is the efficiency of the transform or filter bank employed. The coding gain is frequently used as an effective measure of transform efficiency. For orthogonal transform, the coding gain,  $G_{rc}$ , is given by:

$$G_{\tau} = \frac{\frac{1}{M} \sum_{i=0}^{M-1} \sigma_i^2}{\left( \prod_{i=0}^{M-1} \sigma_i^2 \right)},$$
 (15)

where  $\sigma_i^2$  is the variance of the output at the *i*-th analysis filter. For subband coding, the general coding gain formula is given by [11],

$$G_{SBC} = \frac{1}{M \left( \prod_{i=0}^{M-1} A_k B_k \right)^{1/M}},$$
 (16)

$$A_k = \sum_{j=0}^{N-1} \sum_{i=0}^{N-1} h_k(j) h_k(i) \rho^{|i-j|} \text{ and } B_k = \frac{1}{M} \sum_{j=0}^{N-1} g_k^2(j) . \tag{17}$$

 $h_k(n)$  and  $g_k(n)$  are the impulse responses of the k-th analysis and synthesis filters of length N. Here the input is assumed to be a first-order auto-regressive process with correlation coefficient  $\rho$ .

To reduce the wordlength for the integer implementation, the kernel for  $T_8^{C-H}$ ,  $T_4^{C-H}$  and  $T_4^{S-H}$  are chosen to be 5-bits. Exhaustive search is performed to optimize the coding gain of this ILOT and the optimal solution is ILOT(24,20,12,6,23,7,17,17,7,13,3,6,10,12). The coding gain of this ILOT is 9.16 dB for AR(1) process with correlation coefficient of 0.95. This is close to the coding gain of the LOT, which is 9.22 dB. Fig. 3a and Fig. 3b show respectively the frequency responses of the ILOT(24,20,12, 6,23,7,17,17,7,13,3,6,10,12) and the LOT. The diagonal scaling values  $k_0$  and  $k_1$  are 0.0104 and 0.00002353, respectively. A multiplier-less implementation of this ILOT has also been developed in [13] which requires 106 additions and 42 shifts. Image coding experiments also confirm that its performance is very close to the LOT[13].

# 5. CONCLUSION

In this paper, a new 8-channel integer-valued lapped transform, called the Integer Lapped Orthogonal Transform (ILOT), is presented. The new transform can be implemented using simple integer arithmetic and has very low implementation complexity. Using 5-bits to represent its transform kernel, it was found that good coding gain and stop band attenuation, similar to the LOT, could be achieved. The multiplier-less fast ILOT requires only 106 additions and 42 shifts and can be implemented in 32-bit integer arithmetic [13].

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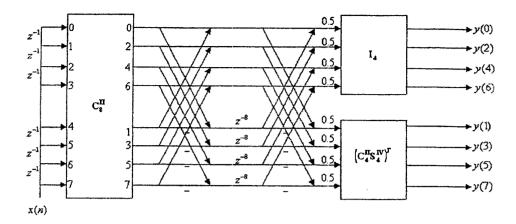


Fig.1 Flow graph of 8-channel LOT

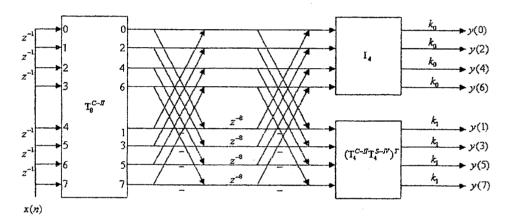


Fig. 2 Flow graph of 8-channel ILOT

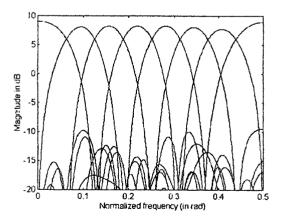


Fig. 3a Frequency response of 8-channel ILOT

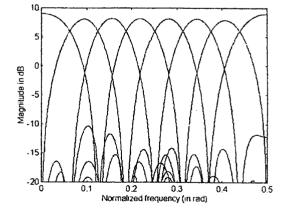


Fig. 3b Frequency response of 8-channel LOT